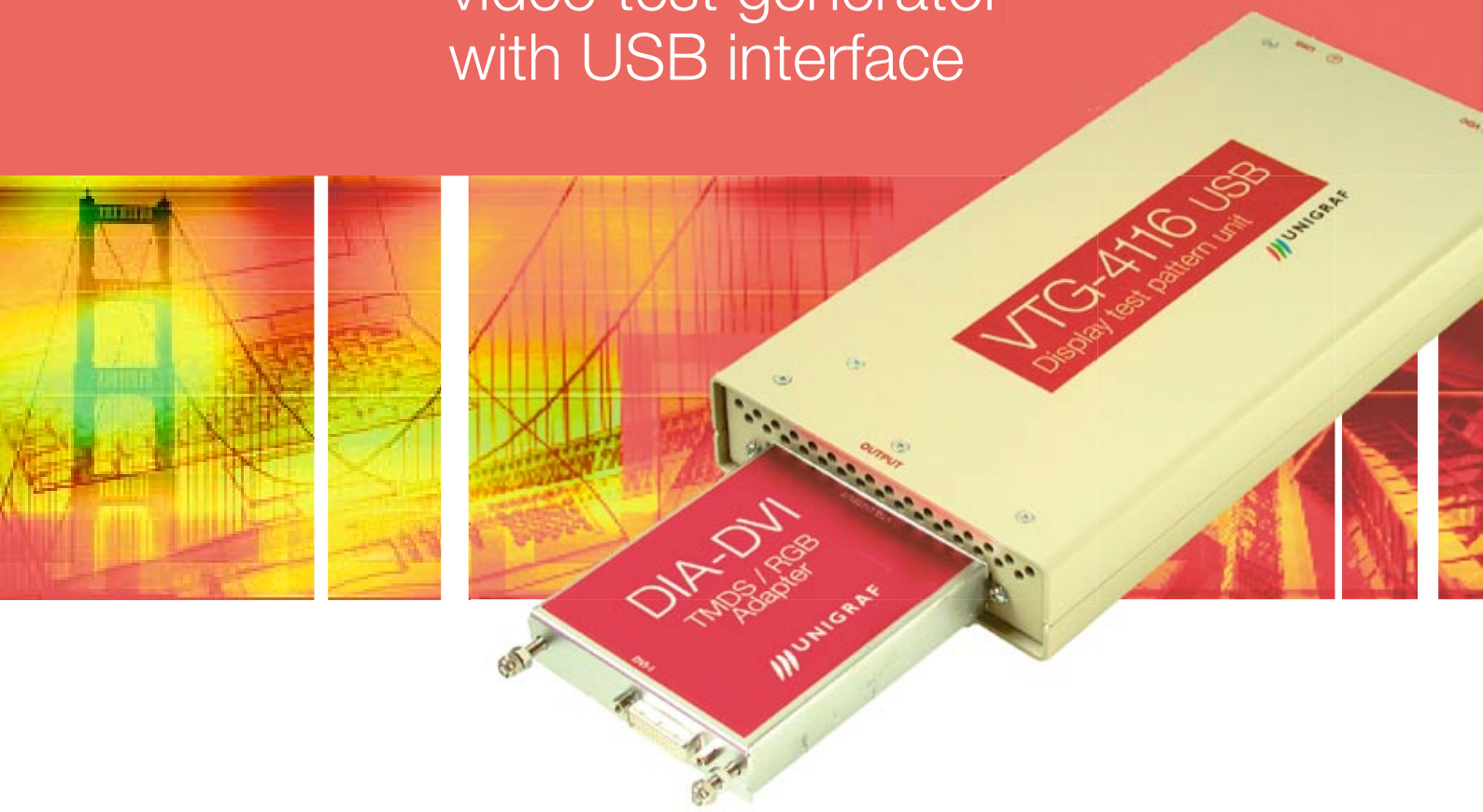
VTG-4116

video test generator with USB interface



Test Signal Generator for all digital, analog and HDTV displays

- Ideal for testing PDP, TFT, LCD, HDTV , DLP and analog input displays
- Very easy to use
- High quality and stable signals
- Easy integration

Applications include:

- manufacturing testing
- engineering signal source
- QA source of standard signals
- servicing
- product performance testing

Powerful programmability and software support

- Maximum 330 MHz pixel frequency
- WinVTG.exe User Interface for Windows (98, 2000 and XP)
- DLL for application programming
- Bitmap support for multiple file formats: .BMP .GIF .JPEG .PCD .PCX .PNG .TIF
- ATE support, VESA DPMS and DDC
- Unlimited number of permanent programmable patterns, timings, colors, palettes, signal formats and sequences

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Specifications

Pixel Frequency

Single-Ended double pixel output mode up to 330MHz Pixel Frequency
Differential mode up to 200MHz
Pixel Frequency Step: 0,01 MHz Accuracy +50 ppm

Graphics Display Memory Size

Resolutions	2048 x 2048 x 8 bit colors out of 16.7 million true color

Horizontal Timing

Scan Range	1 - 1000 kHz
Period	256 - 4096 pixels
Sync Pulse	2 - 2048 pixels
Back Porch	0 - 2048 pixels
Display Resolution	16 - 4080 pixels, active
Adjust Step	1 pixel for all dot clocks

Vertical Timing

Scan Range	10 - 200 Hz
Period	4 - 4500 lines
Sync Pulse	1 - 4095 lines
Back Porch	0 - 4095 lines
Display Resolution	1 - 4200 lines, active
Adjust Step	1 line for all parameters

Outputs

Digital Video	2 x24 bit (3 x 8 bit, RGB)pixels 24 bits differential to 200MHz TTL-level, 50 Ohm termination
Colours	256 simultaneous colours out of 16.7 million 24 bit palette
H&Vsync	TTL-level, 50 Ohm termination
Blank	Composite blanking signal, TTL-level, 50 Ohm termination
Pixel Clock	TTL-level, 50 Ohm termination
Connector	DHP-100 Dsub Half Pitch

All specifications are subject to change

Display Data Format

Scan Modes	Single- or dual-scan
Pixel Clocking	Data on rising edge, on falling edge or on both edges (DDR) 1, 2 or 4 pixels per clock

System Requirements and Software

- Windows™ operating system (95, 98, NT, 2000,XP)
- WinVTG .exe User Interface
- Windows DLL software library
- Visual Basic and C++ sample programs
- USB port
- Power: +12V/1A

(+ output connector supply for +12V max1.5A)

- EMI: meets EN 55011, Class B
- Dimensions: 39 x 140 x 352 mm

Interface Adapters and Direct Interface Adapters

(DIA Adapters mount directly to the VTG card & VIA mount by cable)

- DIA-DVI, serial differential & analog adapter
- DIA-LVDS, serial differential adapter
- DIA-TV, HD& SDTV outputs
- VIA-TTL, parallel adapter with 100 pin cable
- VIA-TMDS, serial differential adapter with 100 pin cable
- VIA-LVDS,serial differential adapter with 100 pin cable

Data File Management

Default Settings	Programmable timing, pattern, color and sequence files at start.

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