VTG-4108 TTL

video source for mobile display testing

Flexible Video Signal Source

The VTG-4108 series of test pattern generators support the need for testing small sized color and monochrome panels. It combines a flexible and user friendly test signal source and a fully configurable LVDS, TTL and Composite PAL/NTSC output. The VTG-4108 TTL provides the a 3 x 8 bit parallel TTL output and a 3 x 6 bit LVDS output. Both common in small sized display modules. A wide variety of data signal configurations and timing modes can programmed to the device.

Supports present and future needs

In addition to the outstanding output signal performance, the VTG-4108 provides superior flexibility by storing timing files, predefined test pattern and custom made bitmaps internally. These features allow the VTG-4108 to support a multitude of different panels and tests.

Powerful programmability and software support

- USB connection to PC
- Powerfull pattern drawing
- Unlimited number of patterns and timings
- Bitmap support
- Configurable TTL output pin assignment

Applications include:

- Manufacturing testing
- Engineering signal source
- QA source of standard signals
- Servicing

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VTG-4108 TTL

video source for mobile display testing

Specifications

Display Memory

2048 x 1024 x 8 bit colors
out of 16.7 million, true color

Pixel Frequency

1 to 80 Mpix/s, step 0,01 MHz,
accuracy +50 ppm

Horizontal Timing

Scan Range	1 - 1000 kHz
Period	256 - 2048 pixels
Sync Pulse	2 - 1024 pixels
Back Porch	0 - 512 pixels
Display Resolution	16 - 1024 pixels, active
Adjust Step	1 pixel for all dot clocks

Vertical Timing

Scan Range	1 to 1000 Hz
Period	4 to 2000 lines
Sync Pulse	1 to 1024 lines
Back Porch	0 to 1024 lines
Display Resolution	1 to 1024 lines, active
Adjust Step	1 line for all parameters

LVDS Output

Format:	21 bit serializer w 18 bit video. Configurable output data mapping.
Transmitter:	Maxim 9209 with 4 LVDS pairs. Clock range of 8 MHz to 54 MHz.
Connector:	26 pin MDR connector with DISM pin-out configuration.

TTL Output

Format:	Parallel TTL output selectable for 3.3V (LVCMOS) or 5V (CMOS) levels. Selectable number of pixels per clock.
Data output:	3 x 8 bits of video, Hsync (+/-), Vsync (+/-), Blank/Enable (+/-) and pixel clock (data on falling/rising/both) Grounds and power on fixed pins. All other signals are software routed to any connection/order.
Power supply:	12 Vdc (0.5 A) and 3.3 / 5 Vdc (1 A)
Connector:	Dual in-line 2.54 pitch 50 pin male header.

System Requirement

Windows™ XP

Power

+12 VDC / 1 A
(External AC/DC adapter included)

Dimensions

390 x 140 x 54 mm

Data Communication

USB 2.0 compatible

SW Support:

WinVTG User Interface
Windows DLL software library
Visual Basic and C++ sample programs

All specifications are subject to change

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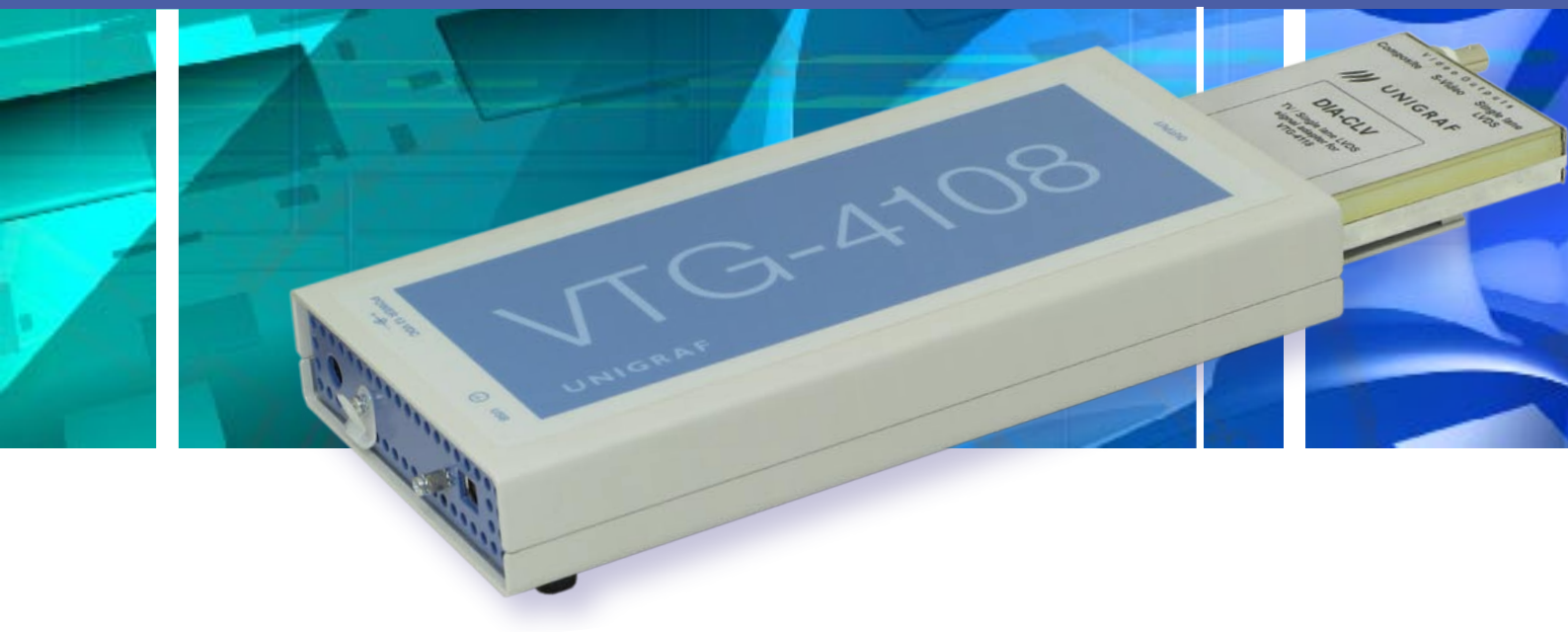
VTG-4108 CLV

video source for mobile display testing



Flexible Video Signal Source

The VTG-4108 supports the need for testing small sized color and monochrome panels. It combines a flexible and user friendly test signal source with Composite (CVBS), S-Video and 24 bit single pair LVDS outputs. A wide variety of data signal configurations and timing modes can programmed to the device.

Supports present and future needs

In addition to the outstanding output signal performance, the VTG-4108 provides superior flexibility by storing timing files, predefined test pattern and custom made bitmaps internally. These features allow the VTG-4108 to support a multitude of different panels and tests.

Powerful programmability and software support

- USB connection to PC
- Powerfull pattern drawing
- Unlimited number of patterns and timings
- Bitmap support

Applications include:

- Manufacturing testing
- Engineering signal source
- QA source of standard signals
- Servicing

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VTG-4108 CLV

video source for mobile display testing

Specifications

Display Memory

2048 x 1024 x 8 bit, 256 colors
out of 16.7 million colors

Composite video (CVBS) and S-Video

Format	PAL and NTSC
Connectors	Composite: BNC S-Video: 4-pin mini DIN

Single pair LVDS

Format	24 bit serializer w clock
Transmitter	DS90C241, clock range 5 to 35MHz, DC-Balanced serializer
Connector	JAE MX38002NQA

Pixel Frequency (LVDS)

5 to 35 Mpix/s, step 0,01 MHz,
accuracy +50 ppm

Horizontal Timing (LVDS)

Scan Range	1 - 1000 kHz
Period	256 - 2048 pixels
Sync Pulse	2 - 1024 pixels
Back Porch	0 - 512 pixels
Display Resolution	16 - 1024 pixels, active
Adjust Step	1 pixel for all dot clocks

Vertical Timing (LVDS)

Scan Range	1 to 1000 Hz
Period	4 to 2000 lines
Sync Pulse	1 to 1024 lines
Back Porch	0 to 1024 lines
Display Resolution	1 to 1024 lines, active
Adjust Step	1 line for all parameters

Power Supply

+12 VDC / 1 A
(External AC/DC adapter included)

Dimensions

390 x 140 x 54 mm

System Requirement

Windows™ XP

Data Communication

USB 2.0 compatible

SW Support:

WinVTG User Interface
Windows DLL software library
Visual Basic and C++ sample programs

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