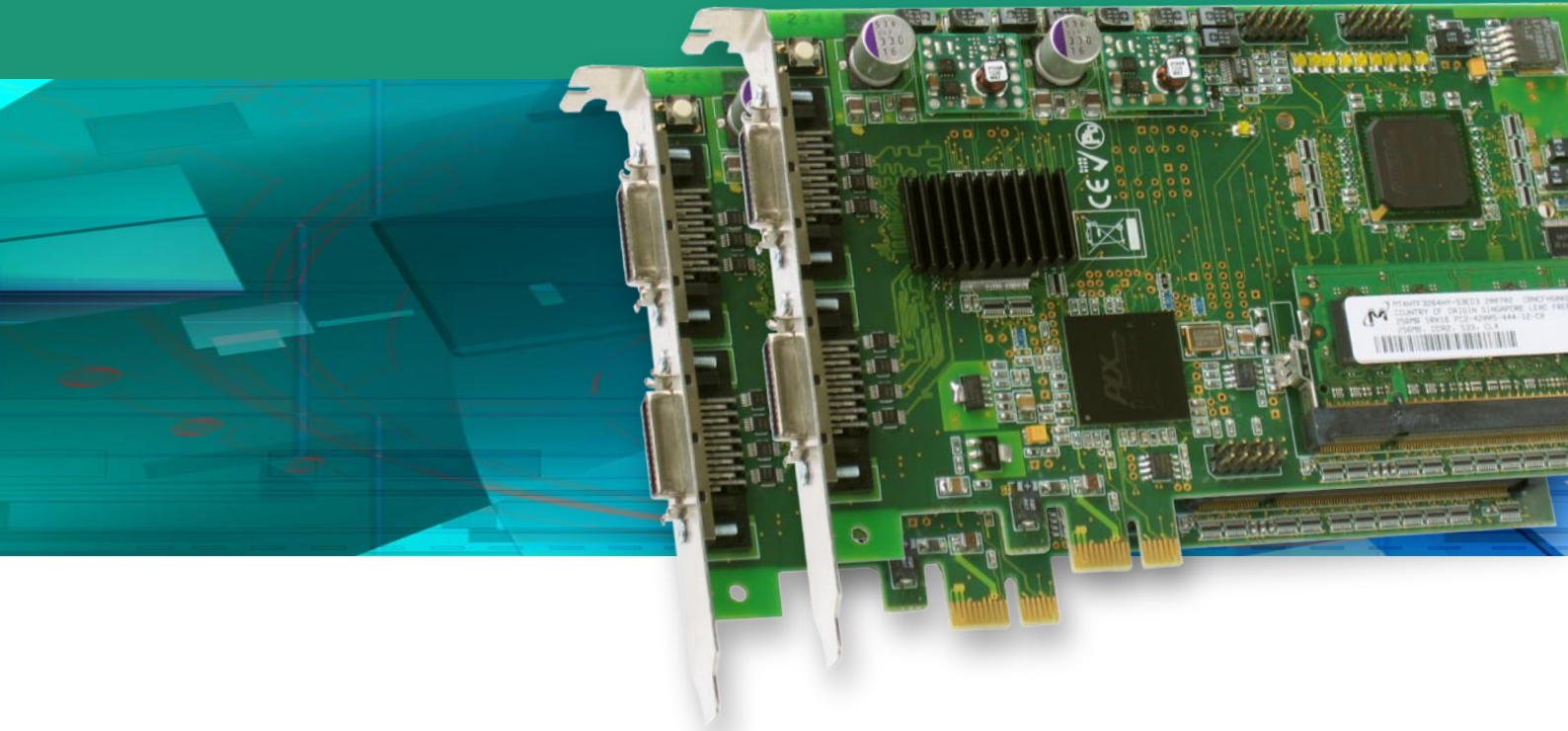# UFG-04 LVDS Quad

four channel LVDS capture

Two Cards, Four Channels

Two syncronized UFG-04 LVDS cards enable the capture of four channel LVDS (Quad LVDS) sources. The synchronization makes sure that the Master and the Slave card are capturing portions of the same input frame. The provided software will combine the portions together to combine a full quad frame for viewing and storage. The maximum capturing speed in four channel LVDS mode is 540 Mpix/s enabling the capture of e.g. 1080p @ 120 Hz input.

UFG-04 LVDS frame grabber enables the capture of high resolution LVDS video with the full 10 bits per color depth. The on-board frame buffer enables the capture of frame-to-frame video clips regardless of the PC bus bottlenecks.

Optimal for Testing Video Interfacing

UFG-04 LVDS is an optimal tool for testing the fidelity of display input electronics. The capture card replaces the actual flat panel display for testing. The flexible design of the internal data handling enables custom data input configurations and advanced on-board image functions.
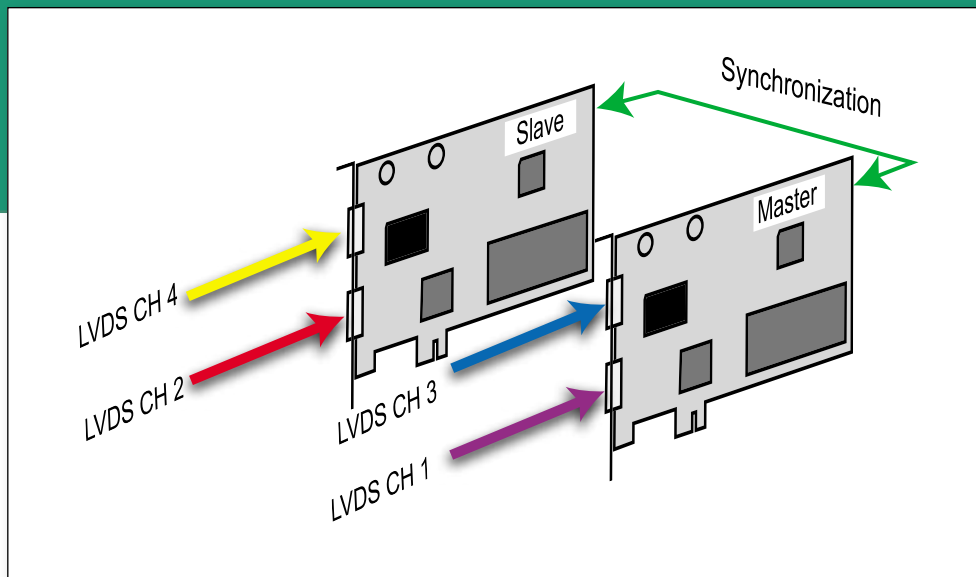
Benefits

- Captures images from High Definition LVDS sources
- 1080p @120Hz capture
- 10 bits per color color
- Two synchronized PCIe cards
- Up to 500 continous 1080p frames

Preliminary

//// **UNIGRAF**

UFG-04 LVDS Quad
four channel LVDS capture

Video Interface Test

Test the output quality of your video input board within seconds. Measure each of the millions of pixels reliably every time. Analyze any test images with the accuracy that your quality criteria demands. Review each individual result in detail and include the long term trends into your quality reports.

Unigraf's unique Video Input Board Test System consists of a Unigraf VTG video pattern source and a Unigraf UFG video frame grabber. The combination provides flexible tools for creation of test functions and sequences to meet your video board testing needs. For more information, contact your local representative or send us an email at info@unigraf.fi.

Preliminary

Specifications

LVDS Video	Dual FPD-Link Receiver (DS90C3202)	
Image size	8 Mpixels maximum	
Pixel Frequency		
Single pixel	135 Mpix/s max	
Dual pixel	UFG-04 HLV	270 Mpix/s max
	UFG-04 LV	165 Mpix/s max
Quad pixel	UFG-04 Quad	540 Mpix/s max
Input Pixel Depth	8 or 10 bits per color	
Custom I/O	3 lines; configurable as input or output	
Connectors	2 x MDR-26 DISM 1.0	
	4 x MDR-26 DISM 1.0 (Quad)	
Output Color Depth	8, 10 or 16 bits per color	
Frame Buffer	2 GBytes	
	2 x 2 GBytes (Quad)	
Data Interface	PCIe™ bus master; sustained transfer rate up to 125 MBytes/s each.	
Operating Systems	Windows® XP	
SW Interface	Custom C/C++ library with functions for accessing the configuration parameters and capturing the image.	
Module Size	107 x 168 mm each	
Power Consumption	10 Watts each	

Please visit www.unigraf.fi for listing of Unigraf Worldwide Distribution